

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (currently amended) A method for optically inspecting and evaluating a sample fabricated with a lithography process, the method comprising:
 - ~~measuring, by optical microscopy, gross overlay between an upper layer and a lower layer of the sample;~~
 - projecting a probe beam at an overlay metrology target included in the sample, where the overlay metrology target includes one or more gratings built into the upper layer of the sample, each of which is paired with a respective grating built into the lower layer of the sample;
 - analyzing diffraction imparted to the probe beam by the gratings of the overlay target to measure fine overlay between the upper and lower layers;
 - measuring the overlay metrology target by optical microscopy to determine the gross overlay between the upper layer and the lower layer of the sample;
 - generating a total overlay measurement that is consistent with both the fine and gross overlay measurements; and
 - using the measurement to control the lithography process.
2. (previously presented) A method as recited in claim 1 in which the gross overlay is measured using images of one or more pairs of gratings.
3. (original) A method as recited in claim 2 in which at least one grating-pair has gratings of different size in the upper and lower layers.
4. (previously presented) A method as recited in claim 1 in which gross overlay is measured using an imaging system that is used for navigation or pattern recognition to locate overlay metrology targets.

5. (original) A method as recited in claim 1 in which the total overlay measurement equals the gross overlay if the gross overlay exceeds a threshold, and the total overlay measurement equals the fine overlay if the gross overlay is equal to or less than the threshold.

6. (previously presented) A method as recited in claim 1 in which the method further comprises the steps of:

- determining a range of unambiguous fine overlay measurements;
- determining from the gross overlay measurement an integer count of whole fine-measurements ranges nearest to the total overlay;
- forming a product by multiplying the integer count by the range fine-measurement range; and
- adding or subtracting the fine overlay measurement to the product.

7. (currently amended) An overlay metrology target that comprises: one or more upper gratings formed on an upper layer of a sample, each grating having a plurality of repeating elements having a period, each paired with a respective lower grating formed on a lower layer of the sample, each grating having a total width dimension (X) and a total length dimension (Y), with at least one grating on the upper layer differing in at least one of the X or Y dimensions than its grating pair an amount sufficient to facilitate measurement of gross overlay by an optical microscope said amount be greater than two periods.

8. (original) An overlay target as recited in claim 7 in which in which one grating is differently sized in the X dimension than its grating pair and one grating is differently sized in the Y dimension than its grating pair.

9. (original) An overlay target as recited in claim 7 in which one grating is differently sized in the X and Y dimensions than its grating pair.

10. (original) An overlay target as recited in claim 7 in which each grating is formed as a parallel series of lines.

11. (original) An overlay target as recited in claim 10 in which the lines in the upper and lower grating of each pair are parallel to each other and at least one grating differs from its pair in the dimension that is parallel to the grating lines.

12. (original) An overlay target as recited in claim 7 in which each grating is formed as a two dimensional array of three dimensional features.

13. (currently amended) A method for controlling overlay of layers within semiconductor wafers created by a lithography process, the method comprising:

forming an overlay metrology target included in a sample, where the overlay metrology target includes one or more gratings built into an upper layer of the sample, each of which is paired with a respective grating built into a lower layer of the sample;

measuring the overlay metrology target to determine gross overlay between the upper layer and the lower layer of the sample;

measuring the overlay metrology target to determine fine overlay between the upper layer and the lower layer of the sample; [[and]]

generating a total overlay measurement that is consistent with both the fine and gross overlay measurements; and

using the measurement to control the lithography process

14. (original) A method as recited in claim 13 in which at least one grating-pair has gratings of different size in the upper and lower layers.

15. (previously presented) A method as recited in claim 13 in which gross overlay is measured using an imaging system that is used for navigation or pattern recognition to locate overlay metrology targets.

16. (original) A method as recited in claim 13 in which at least one grating on the upper layer differs in at least one dimension or shape than its grating pair.

17. (original) A method as recited in claim 16 in which one grating is differently sized in the X dimension than its grating pair and one grating is differently sized in the Y dimension than its grating pair.

18. (original) A method as recited in claim 16 in which one grating is differently sized in the X and Y dimensions than its grating pair.

19. (original) A method as recited in claim 13 in which each grating is formed as a parallel series of lines.

20. (original) A method as recited in claim 19 in which the lines in the upper and lower grating of each pair are parallel to each other and at least one grating differs from its pair in the dimension that is parallel to the grating lines.

21. (original) A method as recited in claim 13 in which each grating is formed as a two dimensional array of three-dimensional features.

22. (currently amended) A method for monitoring overlay of layers in a semiconductor wafer created by a lithography process comprising the steps of:

forming an overlay metrology target included in a sample, where the overlay metrology target includes at least one pair of gratings, one grating of the pair being built into an upper layer of the sample and the other grating of the pair being built into a lower layer of the sample, with the grating in the upper layer differing in at least one dimension or shape from the grating in the lower layer;

measuring the overlay metrology target to determine gross overlay between the upper layer and the lower layer of the sample using optical microscopy;

measuring the overlay metrology target to determine fine overlay between the upper layer and the lower layer of the sample using a scatterometry approach; [[and]]

generating a total overlay measurement that is consistent with both the fine and gross overlay measurements; and

using the measurement to control the lithography process